## Novel 3D Technologies for the Transition to Cognitive Systems

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## Outline

#### Chip-level 3D Technologies

- Introduction
- 3DM for enhanced CMOS performance

#### Enhanced CMOS functionality

- CMOS Silicon Photonics
  - $_{\odot}$  Monolithic laser integration
- Novel hardware for Neural Networks
  - $_{\odot}$  Resistive Crossbar Arrays
  - Photonic Synaptic Processor

#### Conclusions

### What would be the ultimate CMOS Technology?



### Challenge for 3D Monolithic (3DM)



P. Batude, et al., VLSI 2015

### **Temperature flow – bottom to top**



### **Direct Wafer Bonding**



## InGaAs-on-SiGe 3DM Integration



Combined benefits of:

- 3DM integration
- High-mobility channel materials in both layers

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#### **CMOS Silicon photonics**



Integrate electrical & optical functions in silicon

#### $4 \lambda x 25$ Gb/s optical transceiver demonstration





Demonstration of a flip chip mounted 100G transceiver with four wavelength multiplexing at 25 G each.

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- Overcome discrete laser and assembly cost
- New functions, directly combining electronics, passive and active photonics

#### H2020 EU project DIMENSION Output waveguide Silicon photonics Dielectric (ILD 1) Dielectric (ILD 1) Silicon wafer Silicon wafer III-V epitaxial layer stack Reflectors In silicon **CMOS front-end** CMOS front-end Output Silicon photonics waveguide Output waveguide Silicon photonics MZMs AWG AWG MZMs Dielectric (ILD 1) Dielectric (ILD 1) DBR/DFB Silicon wafer Front-end electronics Laser array Silicon wafer **DBR/DFB** laser CMOS front-end CMOS front-end



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#### Processing scheme





Gain section

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### Neuromorphic hardware for big data analytics



#### Today's status on deep neural networks

- Software based on Von Neumann systems
- Training is the bottleneck HPC required
- GPU accelerators processing memory bottleneck

#### Fast and efficient neural network data processing

- 1. Analog approximate signal processing
- 2. Tight integration of processing and memory
  - 10'000x improvement using crossbar arrays

### Hardware implementations

- Electrical crossbar arrays
- Photonic crossbar arrays



### Accelerated learning: Analog crossbar arrays

## Update weight proportional to signals on crossbar row and column

- Increase and decrease of weight
- Symmetric behavior for positive and negative updates
- High weight resolution (~1000 levels) required

Physical challenge: Identify material systems fulfilling those requirements





### Materials for <u>Resistive Crossbar Arrays</u>

#### Synaptic functionality

- Multiply: Ohm's law; Accumulate: Kirchhoff's law
- Weight update: Tunable resistance

#### Tunable resistance

- State of the art: filamentary oxides, PCM
  - Not analog enough or not symmetric enough
- Search for <u>uniform</u> effects leading to  $\Delta G$  with the appropriate resolution and symmetry
  - Electrochemical (de)intercalation
  - Control domain structure in ferroelectrics





### **EU project NEURAM**

Novel 3DM technology for neural networks



## Photonic crossbar unit

- Alternative crossbar physical principle leveraging the photorefractive effect
  - Demonstrated in 3D free space photonic neural networks in the 90s
    - i.e. Hughes Research Laboratories
  - New developments we can leverage
    - Integrated optic technology
    - Co-integration of new materials
- Non-volatile weights applying the photorefractive effect
  - Grating writing by interference of optical plane waves in an electro-optic material Strength proportional to product of the amplitudes of the writing beams
  - Written grating acts as the synaptic interface between plane optical waves



2 source signals





# Photonic crossbar unit - operating principle



#### Photonic crossbar



Writable photorefractive gratings provide the same functionality as the tunable resistive elements in a crossbar unit



## Photonic crossbar unit - subsystem layout

#### Design study: 200 x 200 processing unit

#### Weighting elements

- Thin planar photorefractive slab on SOI, 1mm x 1mm
- BTO or GaAs

#### 200 Pre-synaptic electrical input signals

- Optical power control through silicon photonic eo modulators
- Plane optical waves under different angles in Si slab layer

#### 200 Post-synaptic electrical output signals

- Diffracted plane wave outputs are focused by Si mirror
- Converted to electrical signals by a Si photonic detector array

#### Weight adjustment

 Photorefractive index modulation through interference pattern originating from the two transmitter arrays

### Scalability

- A 200 x 200 processing unit fits in 5mm x 5mm
- Theoretical capacity of interaction area is 2000 x 2000
- Linear width and height scaling with # channels



### Conclusions

- CMOS technology performance enhancement
- 3D Monolithic integration using III-V Materials
  - Technology established

#### Enhanced silicon photonics

- Monolithically integrated lasers

#### Novel hardware for neural networks

- Co-integrated analog processing and memory
- Electrical and Optical implementations of the synaptic processing unit

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