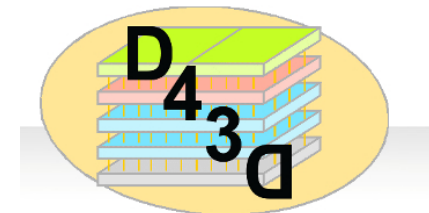


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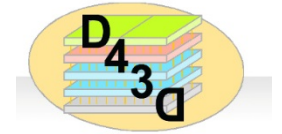
D43D'2018

10th Workshop on Design for 3D Silicon Integration

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D43D Panel

- **« 3D Technology : which road for which disruptive architectures ? »**
- Panelists :
 - Subhasish Mitra, Univ. Stanford, USA
 - Paul Franzon, NCSU, USA
 - Marcel Wieland, Global Foundry, Germany,
 - Francois Jacquet, KALRAY, France,
 - Denis Dutoit, CEA-LETI, France